IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): YAMAGUCHI

Serial No.: 10/621,488

Filed: July 18, 2003

tle: SEMICONDUCTOR DEVICE INCLUDING

POWER MOSFET AND PERIPHERAL DEVICE AND METHOD FOR MANUFACTURING THE SAME

Commissioner for Patents Arlington, VA 22202

Atty. Dkt.: 01-116-DIV

Group Art Unit: 2811

Examiner: Unknown

Date: February 12, 2004

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Sir:

Pursuant to 37 C.F.R. §1.56, the references listed on the attached Form PTO-1449 are being submitted for consideration by the Examiner without any admission that they constitute statutory prior art, or without any admission that they contain subject matter that anticipates the invention or renders the invention obvious to a person of ordinary skill in the art.

Further, pursuant to 37 C.F.R. §1.97(e), the undersigned hereby certifies that each listed reference was first cited in a Communication (copy enclosed) from a foreign patent office in connection with a counterpart foreign application not more than three months prior to the filing of this statement.

Although no fees are believed to be due at this time, it is respectfully requested that any such fees be charged to Deposit Account 50-1147. Also, the Examiner is requested to initial the attached PTO Form-1449 and to return a copy of same to the undersigned attorney as proof that the listed references have been considered and made of record.

Respectfully submitted,

David G. Posz

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* PATENT APPLICATION

| | 61 | PE | | | | Page 1 of 1 |
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| FORM PTO-1449 | | 45.17 | ATTY. DKT NO. | 01-116-DIV | SER. NO. | 10/621,488 |
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REFERENCE DESIGNATION U.S. PATENT DOCUMENTS

| EXAMINER INITIAL | DOCUMENT NUMBER | DATE | NAME | CLASS | SUB CLASS |
|---------------------|--------------------|------|------|-------|--------------|
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FOREIGN PATENT DOCUMENTS

TRANSLATION

| | | | | | | 110 | ANSLATION | |
|--|-----------------|----------|---------|------|-------|--------------|-----------------|----|
| | DOCUMENT NUMBER | DATE | COUNTRY | NAME | CLASS | SUB CLASS | YES | NO |
| | JP-A-S58-164258 | 9/29/83 | JAPAN | | | | X (Abstract) | |
| | JP-A-H03-82138 | 04/08/91 | JAPAN | | | | X (Abstract) | |
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* Full English text of the JP Document will be available in machine-translated form from JP (Japanese Patent Office) English language web site at http://www1.ipdl.jpo.go.jp/PA1/cgi-bin/PA1INDEX.

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

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| EXAMINER | DATE CONSI | DERED | |

Rev. 10/94 (Form 3.05)